

02/02/00

ISSUE CLASSIFICATION	
Class	Subclass

TW

PATENT NUMBER

U.S. UTILITY Patent Application

KW O.I.P.E. TdZ SCANNED _____ G.A. <u>mmw</u>	PATENT DATE
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APPLICATION NO. 09/496421	CONT/PRIOR YES	CLASS 257	SUBCLASS 202	ART UNIT 2815	EXAMINER LEE WILSON
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APPLICANTS

Ritsuko Iwasaki

TITLE

Semiconductor device having an improved layout pattern of pair transistors

PTO-2040
12/99**ISSUING CLASSIFICATION**[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) _____ (Date)		NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____	_____ (Primary Examiner) _____ (Date)		ISSUE FEE	
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